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October 6, 1998Priority GROUP  
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## U.S. PATENT DOCUMENTS

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
ON	AA	3,660,735	05/1972	McDougall	317	235R	
UN	AB	4,697,333	10/1987	Nakahara	437	20	
UN	AC	4,760,033	07/1988	Mueller et al.	437	34	
UN	AD	4,859,618	08/1989	Shikata et al.	437	41	
UN	AE	5,185,280	02/1993	Houston et al.	437	44	
UN	AF	5,244,823	09/1993	Adan	437	41	
UN	AG	5,321,283	06/1994	Cogan et al.	257	256	
UN	AH	5,395,773	03/1995	Ravindhran et al.	437	27	
UN	AI	5,401,994	03/1995	Adan	257	335	
UN	AJ	5,424,567	06/1995	Chen	257	315	
UN	AK	5,482,878	01/1996	Burger et al.	437	41	
ON	AL	5,147,811	09/1992	Skagami	438	302	

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							Yes	No
UN	AM	0-298023	10/1990	Japan				
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## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

UN	AR		Jung, D. et al., "A 0.25 $\mu$ m CMOSFET Using Halo Implantation for 1Gb DRAM", Extended Abstracts of the 1995 Internatl. Conf. on Solid State Devices and Materials, Osaka, JP, published Sept. 1995, pp. 869-871.
UN	AS		"Simplified Lightly Doped Drain Process"; IBM Technical Disclosure Bulletin, Vol. 30 No. 12, pp. 180-181
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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE			ATTY. DOCKET NO. MI22-1885		Priority SERIAL NO. 09/167,174	
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ON	AA	5,217,910	06/1993	Shimizu et al.	438	231		
ON	AB	5,500,379	03/1996	Odake et al.	438	232		
ON	AC	5,614,432	03/1997	Goto	438	231		
ON	AD	4,356,623	11/1982	Hunter				
ON	AE	4,703,551	11/1987	Szluk et al.				
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		Document Number	Date	Country	Class	Subclass	Translation	
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ON	AM	364,037,055	02/1989	JP (Suzuki)	257	344		
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ON	AO	000,071,335	02/1983	EP (Haskell)	257	344		
ON	AP	363,095,670	04/1988	JP (Ezaki)	257	344		
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ON	AC	4,149,904	04/1979	Jones	148	1.5	
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ON	AH	5,866,460	02/1999	Akram et al.	438	306	
ON	AI	6,044,843	4/2000 <del>12/1999</del>	Huang	438	241	
ON	AJ	5,534,449	1/1996 <del>12/1993</del>	Dennison et al			
ON	AK	5,272,367	12/1993 <del>07/1996</del>	Dennison et al			
ON	AL	4,949,136	08/1990	Jain			
ON	AM	5,405,791	04/1995	Ahmad			
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	AP							
	AQ							

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